


**INFORMATION DISCLOSURE
CITATION**

ATTY. DOCKET NO.

APR 06 2009

SERIAL NO.

1035-660

10/593,695

APPLICANT

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(Use several sheets if necessary)

FILING DATE

GROUP

September 20, 2006

2183

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	6415380 B1	07/2002	Sato			
			(corresponds to JP 11-212788 submitted with IDS on 09/20/2006)			
	6810474 B1	10/2004	Miki			
			(corresponds to WO 99/45463 submitted with IDS on 09/20/2006)			

FOREIGN PATENT DOCUMENTS

DOCUMENT	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES NO
2001-5678A	12/2001	Japan			x

OTHER DOCUMENTS (including Author, Title, Date, Pertinent pages, etc.)

	Yasuhiko Nakashima, et al., "Multilevel Reuse and Parallel Precomputation Based on Dynamic Instruction Analysis", Article of Journal of Information Processing Society, Computing System, Information Processing Society, July 2003, Vol. 44, No. SIG_10 (ACS_2) (20030715), pp. 1-16 (together with its partial English translation)
	Notice of Allowance for Japanese Patent Appln. No. 2004-097197 mailed January 20, 2009 with English translation

*Examiner	/Idriss Alrobaye/	Date Considered	02/09/2010
Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Initial a this form with next communication to applicant.			